



Docket No.: M4065.0901/P901  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Hagop A. Nazarian

Application No.: 10/699,652

Confirmation Number: 3940

Filed: November 4, 2003

Art Unit: 2827

For: SERIAL TRANSISTOR-CELL ARRAY  
ARCHITECTURE

Examiner: T. Nguyen

**SUBMISSION OF DRAWINGS**

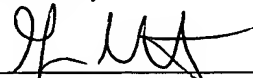
MS PGPUB Drawings  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Submitted herewith is one set (eight sheets, eight figures) of drawings for filing in the above-identified patent application. Kindly substitute the enclosed drawings for the drawings submitted with the originally filed application.

Dated: June 22, 2005

Respectfully submitted,

By 

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